**SURAJ SAI VEGUNTA**

**PROFILE**

A highly motivated, upcoming electronics and communication engineer, seeking an Entry level

in an organization preferably in the field of Programming, Digital Electronics, Circuit Theory, and related areas, which provide me with ample opportunities to enhance my skills and knowledge along with contributing to the growth of the organization so as to grow professionally along with the organization.

**EDUCATION**

* **B.Tech Electronics Communication Engineering**

**CGPA – 7.17 / 10 2019-2023**

Amrita Vishwa Vidyapeetham

* **Class 12** – 95% **2019**

Institution: Sasi Educational Institutes

* **Class 10** – 94% **2017**

Institution: Sri Somanatha Greenfields Public School

**TECHNICAL INTERESTS**

Digital electronics, Circuit Theory, VLSI

**PROJECTS**

**Driver Drowsiness Detection System**

·Duration/Period: 07/06/2022

·Objective: The main agenda of the project is to detect the drowsiness of the driver and alert him with an alarm. This majorly prevents the probability of accidents at night times which is identified as the common problem that causes the chaos. In addition to that the speed of the of the vehicle decreases if the driver does not respond to the alarm and a warning is displayed on a LCD screen to bring attention for the surrounding vehicles

·Tools Or Techniques Used: DC Motors, Connecting Wires, Proteus, Arduino, IR sensor, Buzzer, L298D Motor Driver, LCD

·Outcome: Drowsiness Detection

**TECHNICAL SKILLS**

C, Java

**ACHIEVEMENTS & HONOURS**

AYUDH – (Awaken Youth Unite for Dharma)  
·Area / Topic / Details:AYUDH empowers young people to integrate universal values into their daily lives. Starting with themselves, the goal is to establish a future of hope, peace and social engagement.

·When & Where: Amrita Vishwa Vidyapeetham 2019-2020

**LANGUAGES**

English, Telugu, Hindi, Tamil.